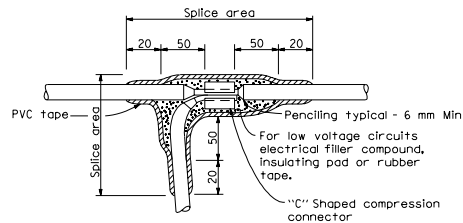




DIST.	COUNTY	ROUTE	KILOMETER POST TOTAL PROJECT	SHEET NO.	TOTAL SHEETS

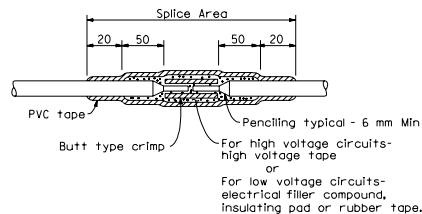
RED SEALED ELECTRICAL ENGINEER
 July 1, 1999
 PLANS APPROVAL DATE
 The State of California or its officers or agents shall not be responsible for the accuracy or completeness of electronic copies of this plan sheet.

PROFESSIONAL ENGINEER
 Samuel Martinez
 No. E13633
 Exp. 6-30-03
 ELECTRICAL
 STATE OF CALIFORNIA



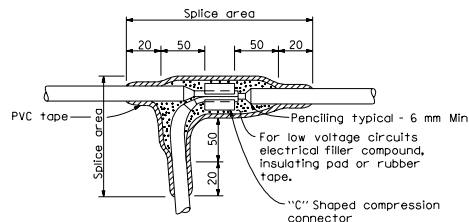
TYPE "C" SPLICE

Between 1 free-end and 1 through conductor



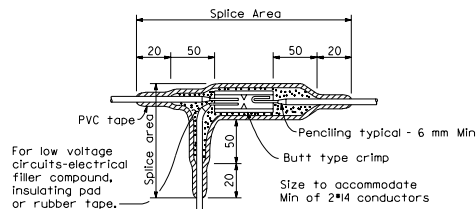
TYPE "S" SPLICE

Between 2 free-ends



TYPE "T" SPLICE

For 3 free-ends



TYPE "ST" SPLICE

NOTES

1. All dimensions are minimum.
2. Rubber tapes shall be rolled after application.

INSULATION METHODS

Low Voltage Circuits (0-600 V)

METHOD "A" (Used only when specified)

1. Completely cover the splice area with electrical insulating coating and allow to dry.
2. Apply electrical filler compound with minimum thickness of 4 mm.
3. Apply 3 layers half lapped polyvinyl chloride tape.
4. Cover entire splice with electrical insulating coating and allow to dry.

OR

METHOD "B"

1. Completely cover the splice area with electrical insulating coating and allow to dry.
2. Apply 2 layers of electrical insulating pad with minimum thickness of 4 mm each layer or 2 layers, half lapped, synthetic oil resistant, self fusing rubber tape.
3. Apply 3 layers half lapped polyvinyl chloride tape.
4. Cover entire splice with electrical insulating coating and allow to dry.

High Voltage Circuits (Over 600 V)

1. Completely cover the splice area with electrical insulating coating and allow to dry.
2. Apply high voltage tape to a minimum thickness equal to original insulation.
3. Apply 3 layers half lapped polyvinyl chloride tape.
4. Cover entire splice with electrical insulating coating and allow to dry.

SIGNAL, LIGHTING AND ELECTRICAL SYSTEMS SPlicing DETAILS

NO SCALE

ALL DIMENSIONS ARE IN
MILLIMETERS UNLESS OTHERWISE SHOWN

ES-13A

1999 STD. PLAN ES-13A